Table H‑12 – Association of ctxIdx and syntax elements for each initializationType in the initialization process

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  | **Syntax element** | **ctxIdxTable** | **initType** | | |
| **0** | **1** | **2** |
| coding\_unit( ) depth\_mode\_parameters( ) | depth\_intra\_mode |  | 0..7 | 8..15 | 16..23 |
| wedge\_full\_tab\_idx |  | 0 | 1 | 2 |
| wedge\_predtex\_tab\_idx |  | 0 | 1 | 2 |
| dmm\_delta\_end\_flag dmm\_delta\_end\_abs\_minus1 |  | 0 | 1 | 2 |
| dmm\_dc\_flag |  | 0 | 1 | 2 |
| dmm\_dc\_abs |  | 0 | 1 | 2 |
| edge\_code |  | 0 | 1 | 2 |
| iv\_res\_pred\_weight\_idx |  |  | 0..3 | 4..7 |
| ic\_flag |  |  | 0 | 1 |
| edge\_dc\_flag |  | 0 | 1 | 2 |
| edge\_dc\_abs |  | 0 | 1 | 2 |
| sdc\_residual\_flag |  | 0 | 1 | 2 |
| sdc\_residual\_abs\_minus1 | ~~prefix:  suffix: na~~ | ~~prefix:~~0 ~~suffix: na~~ | ~~prefix:~~1 ~~suffix: na~~ | ~~prefix:~~0 ~~suffix: na~~ |
| inter\_sdc\_flag |  | 0 | 1 | 2 |
| inter\_sdc\_resi\_abs\_minus1 |  | 0 | 1 | 2 |
| inter\_sdc\_resi\_sign\_flag |  | 0 | 1 | 2 |

Table ‑28 – Syntax elements and associated types of binarization, maxBinIdxCtx, ctxIdxTable, and ctxIdxOffset

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Syntax element** | **initType** | **Type of binarization** | **maxBinIdxCtx** | **ctxIdxTable** | **ctxIdxOffset** |
| sdc\_residual\_abs\_minus1 | 0 | TU, cMax=5  ( specified in subclause ) | ~~prefix:~~ 0~~,  suffix: na~~ | ~~prefix:  suffix: na~~ | ~~prefix:~~ 0 ~~suffix: na, (Bypass)~~ |
| 1 | ~~prefix:~~ 0~~,  suffix: na~~ | ~~prefix:~~  ~~suffix: na~~ | ~~prefix:~~ 1 ~~suffix: na, (Bypass)~~ |
| 2 | ~~prefix:~~ 0~~,  suffix: na~~ | ~~prefix  suffix: na~~ | ~~prefix:~~ 2 ~~suffix: na, (Bypass)~~ |

~~H.9.3.2.1.10 Binarization process for sdc\_residual\_abs\_minus1~~

1. ~~Input to this process is a request for the a syntax element sdc\_residual\_abs\_minus1,~~
2. ~~Output of this process is the binarization of the syntax element.~~
3. ~~The bin string is a concatenation of a prefix bin string and, when present, a suffix bin string.~~
4. ~~The variable numDepthValues is derived as follows:~~ 
   1. ~~numDepthValues = dlt\_flag[ nuh\_layer\_id ] ? (‑242)  
       num\_depth\_values\_in\_dlt[ nuh\_layer\_id ] : ( 1  <<  BitDepth~~~~Y~~~~) − 1~~
5. ~~The variable cMaxPrefix is derived as follows:~~ 
   1. ~~cMaxPrefix = ( numDepthValues \* 3 )  >>  2 )~~
6. ~~For the derivation of the prefix bin string, the following applies:~~

* ~~If sdc\_residual\_abs\_minus1 is less than cMaxPrefix, the prefix bin string is a bit string of length sdc\_residual\_abs\_minus1+ 1 indexed by binIdx. The bins for binIdx less than sdc\_residual\_abs\_minus1 are equal to 1. The bin with binIdx equal to sdc\_residual\_abs\_minus1 is equal to 0.~~
* ~~Otherwise, the prefix bin string is a bit string of length cMaxPrefix with all bins being equal to 1.~~

~~When sdc\_residual\_abs\_minus1 is greater than cMaxPrefix, the suffix of the bin string is present and it is derived as follows:~~

* + ~~The suffix value suffixVal, is derived as follows:~~
    1. ~~suffixVal = sdc\_residual\_abs\_minus1 − cMaxPrefix  (‑243)~~
  + ~~The suffix of the bin string is specified by Fixed-length (FL) binarization process as specified in subclause with suffixVal and cMax equal to ( numDepthValues − cMaxPrefix ) as the inputs.~~

Table H‑31 –Assignment of ctxIdxInc to syntax elements with context coded bins

| **Syntax element** | **binIdx** | | | | | |
| --- | --- | --- | --- | --- | --- | --- |
| **0** | **1** | **2** | **3** | **4** | **>=5** |
| wedge\_full\_tab\_idx | 0 | 0 | 0 | 0 | 0 | 0 |



|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| dmm\_dc\_flag | 0 | na | na | na | na | na |
| dmm\_dc\_abs | 0 | 0 | 0 | 0 | 0 | 0 |
| dmm\_dc\_sign\_flag | bypass | 0 | 0 | 0 | 0 | 0 |
| edge\_dc\_flag | 0 | na | na | na | na | na |
| edge\_dc\_abs | 0 | 0 | 0 | 0 | 0 | 0 |
| edge\_dc\_sign\_flag | bypass | 0 | 0 | 0 | 0 | 0 |
| edge\_code | 0 | 0 | 0 | 0 | 0 | 0 |
| res\_pred\_flag | 0 | na | na | na | na | na |
| ic\_flag | 0 | na | na | na | na | na |
| sdc\_residual\_flag | bypass | na | na | na | na | na |
| sdc\_residual\_abs\_minus1 | ~~prefix:~~ 0 ~~suffix: bypass~~ | ~~prefix:~~ 0 ~~suffix: bypass~~ | ~~prefix:~~ 0 ~~suffix: bypass~~ | ~~prefix:~~ 0 ~~suffix: bypass~~ | ~~prefix:~~ 0 ~~suffix: bypass~~ | ~~prefix:~~ 0 ~~suffix: bypass~~ |
| inter\_sdc\_flag | 0 | 0 | 0 | 0 | 0 | 0 |
| inter\_sdc\_resi\_abs\_minus1 | 0 | 0 | 0 | 0 | 0 | 0 |
| inter\_sdc\_resi\_sign\_flag | 0 | 0 | 0 | 0 | 0 | 0 |