

JCT3V-E0230: AHG13: MFC Conformance Testing

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Conformance Bitstreams for MFC High Profile

- Various combinations of syntax elements introduced by MFC

Categories	Bitstream	Frame Rate (f/s)	mfc_format_idc	default_grid_ position_flag	rpu_filter_ enabled_flag	rpu_field_ processing_flag
RPU Operational Modes	MFCRFT-1	24p	0	1	1	0
	MFCRFT-2	24p	0	1	0	0
RPU Interlaced Coding Tools	MFCFLD-1	50i	0	1	1	1
	MFCFLD-2	50i	0	1	1	1
MFC Format	MFCMFM-1	24p	1	1	1	0
Grid Position	MFCGRD-1	24p	0	0	1	0

MFC Conformance Bitstream Package

- Each zip file package:
 - a bitstream (.264)
 - a short description (.txt)
 - an md5sum (md5sum.txt) :
 - bitstream
 - decoded BL and EL yuv (normative)
 - fully reconstructed left and right view yuv (non-normative).
- Bitstream generation
 - Software in E0231: encoder_mfc_conformance.cfg
 - Sequences: progressive: 1080 24p; interlace: 1080 50i
 - Test condition:
 - 17 frames are encoded with one I frame only.
 - GOP size is set to 8 with hierarchical B coding structure.