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| **Joint Collaborative Team on 3D Video Coding Extension Development**  **of ITU-T SG 16 WP 3 and ISO/IEC JTC 1/SC 29/WG 11**  1st Meeting: Stockholm, SE, 16–20 July 2012 | Document: JCT2-A0009 |

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| *Title:* | **3D-CE1.h Summary Report: View Synthesis and Inter-view Prediction** | | |
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| *Purpose:* | Report | | |
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| *Source:* | Summary Report | | |

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# Abstract

This document is the summary report of Core Experiment 1 in High Efficiency Video Coding (HEVC) based 3D Video Coding. The Core Experiment was established at the 100th MPEG Meeting in Geneva, Switzerland to investigate the impact of view synthesis based and inter-view prediction on texture and depth coding for the dependent views.

The methods to be investigated needed to be implemented into the 3DV-HTM 3.1 reference software [[1](#Hei12)] and evaluated according to the common test conditions. [[2](#Hei11)]

# Participants

During the 100th MPEG Meeting the following participants registered for this Core Experiment:

* RWTH Aachen University
* Poznan University of Technology
* Samsung
* LG Electronics
* Mitsubishi Electric
* NTT

After the meeting the coordination of the experiments to be conducted was done via email.

RWTH Aachen University, LG Electronics and Samsung decided to not have a proposal tested in this Core Experiment during this meeting cycle. Poznan University of Technology also wanted to have a proposal during this meeting cycle, but their software was not ready for being cross-checked.  
Samsung offered to be a cross checker for the CE. The following proposal/cross checker combination has been assigned:

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| Proponent | Cross Checker |
| NTT/Mitsubishi Electric | Samsung |

# Tool Description

The tools to be investigated in this Core Experiment use the decoded texture and depth information from the independent base view to compute a prediction signal for the texture and depth component of the dependent views. This process is done by means of a warping algorithm, which is similar to the non-normative algorithms used in the final view synthesis stage at the receiver.

As the view synthesis, which is required to form the described prediction signal, is part of the coding loop, this view synthesis algorithm is a normative tool and may improve the coding efficiency of a 3D video codec.

## Description of Proposed Tool by NTT/Mitsubishi Electric

View synthesis prediction (VSP) is a technique to remove inter-view redundancies when coding multiview video information, which uses a synthetic picture as a reference picture to predict the current picture. This contribution first proposes inserting synthetic pictures to reference picture lists, which shows an average bitrate saving of -2.6% and -0.4% for the dependent views, with maximum gains of -9.9% and -5.8% for these views. This contribution further proposes a VSP skip mode, which enables the skip mode relative to the synthetic reference picture. However, this mode has not been fully implemented and requires further study. The benefit of VSP for depth coding has also been investigated, but the result is not yet conclusive. Lastly, this contribution proposes to simplify the rendering process in HTM, which would reduce the decoding time by 9% when VSP is enabled.

The proponent recommends to adopt the basic VSP scheme by adding the synthetic pictures to the reference picture lists and to adopt the simplified view synthesis procedure into HTM. It is also suggested to continue study VSP skip mode and simplify synthetic picture generation in the context of this CE.

# Simulation Results

The core experiment investigates the effects of view synthesis based prediction in terms of bitrate reduction for the dependent views. The proposed methods for view synthesis based prediction are implemented into the 3DV-HTM 3.1 software and evaluated according to the common test conditions. [[2](#Hei11)]

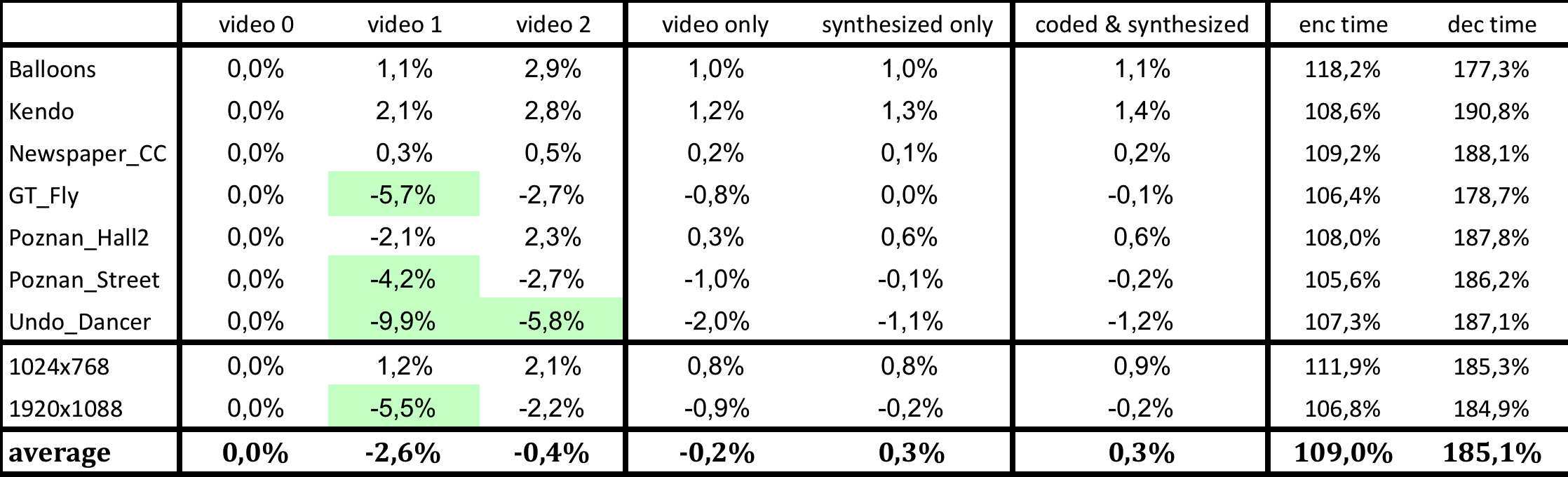
## Results of VSP Tool by NTT/Mitsubishi Electric

The joint contribution from NTT and Mitsubishi Electric includes seven different configurations, which use different variants of view synthesis prediction: Basic VSP scheme by adding VSP reference picture to the reference picture lists; VSP skip mode; VSP for depth component; and simplifications of the rendering process. From the results of the current simulation, adding the synthesized pictures for texture (i.e. test case 2 in the contribution) yields the best performance with an average bitrate saving of -2.6% and -0.4% for the dependent views. Maximum gains can be observed for the sequence Undo\_Dancer with -9.9% and -5.8%, as shown in the following table.

More study is needed for VSP skip mode and VSP for depth components, as the implementations are still uncompleted. In addition, the simplification of the rendering process in HTM leads to about 9% less decoding time.

### Basic VSP for texture

The following table shows the results for using basic view synthesis prediction by putting a synthesized view into the reference picture list of the texture view to be coded.



### Simplified rendering process

The decoding time is reduced by the proposed simplification of the rendering process. Test cases 4, 5 and 6 correspond to test cases 1, 2 and 3, respectively, but with a simplified rendering. From the table below, it could be found that the decoding time is reduced by about 9%.



## Cross Check of NTT/MERL by Samsung

The results of Samsung’s cross check shows a perfect match of the results presented above and with the additional configurations of the contribution document. At the time of submission of this summary report the crosscheck was only finished for a subset of all test sequences, as the other simulations were still ongoing.

# References

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| [1] | Krzysztof Wegner Heiko Schwarz, "Test Model under Consideration for HEVC based 3D video coding v3.0," ISO/IEC JTC1/SC29/WG11 MPEG, N12744 2012. |
| [2] | Dmytro Rusanovskyy Heiko Schwarz, "Common Test Conditions for 3DV experimentation," ISO/IEC JTC1/SC29/WG11 MPEG, N12745 2012. |

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