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| *Title:* | **Mental Cross-check of JCTVC-J0164** | | |
| *Status:* | Input Document to JCT-VC | | |
| *Purpose:* | Information | | |
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| *Source:* | SHARP | | |

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# Abstract

This document provides a mental cross-check for JCTVC-J0164 - Simplified signalling of MSB cycle [1].

# Cross-check

The proposal in JCTVC-J0164 is claimed to have been understood by the author of this document.

A summary of the author’s understanding of the proposal in JCTVC-J0164 is as follows:

The proposal claims that that delta\_poc\_msb\_present\_flag can be inferred by inspecting the value of poc\_lsb\_lt such as the flag shall equal 1 if poc\_lsb\_lt[i] equals to poc\_lsb\_lt [j] for any possible j value (i.e. j is not equal i). Otherwise, the flag shall equal 0.

The proposal proposes to:

* Remove delta\_poc\_msb\_present\_flag
* Rename delta\_poc\_msb\_cycle\_lt to delta\_poc\_msb\_cycle\_idc

# References

1. Hendry, B.M. Jeon “Simplified signalling of MSB cycle,” 11- 20 July, 2012, JCTVC-J0164