

Non-CE6.c: Fixed length binarization of remaining intra prediction mode

JCTVC-H0081

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1.

1. Overview

Overview

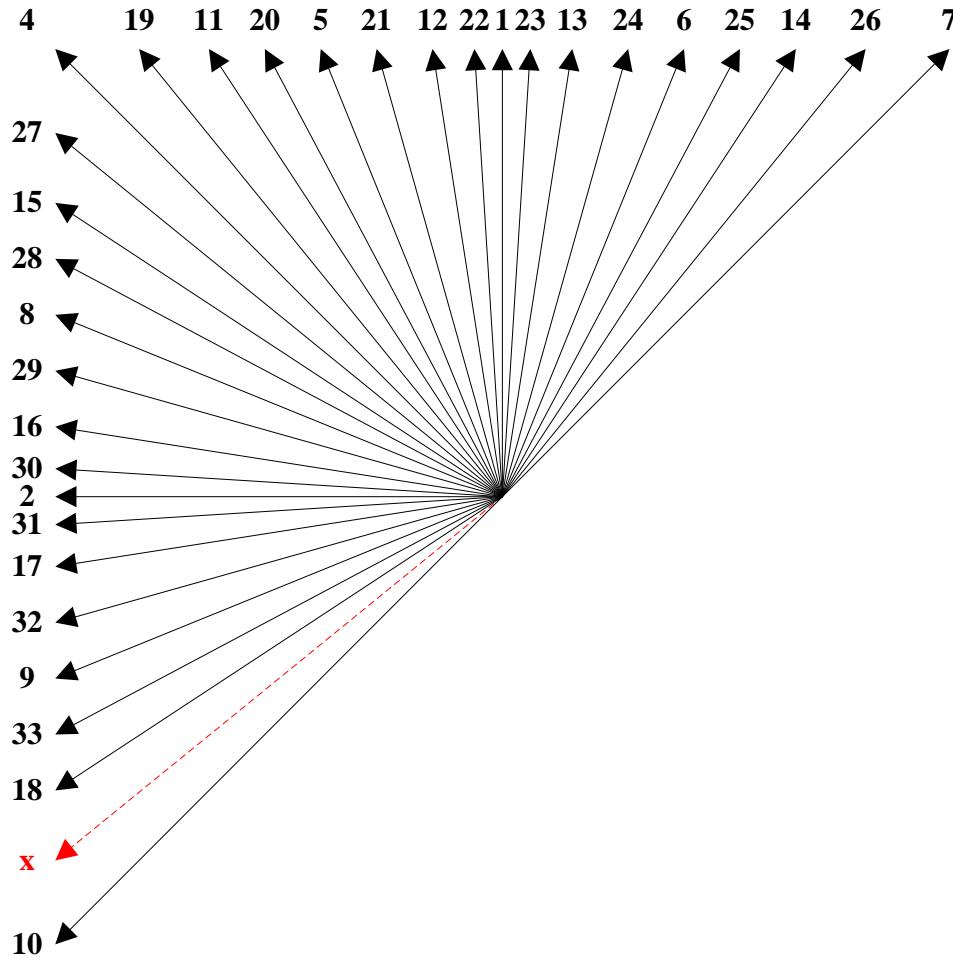
- Proposed technique
 - Fixed length binarization of remaining intra prediction mode coding
- Algorithm
 - Remove intra prediction mode #34
 - Remove the escape code for remaining intra prediction mode
- Crosscheck
 - JCTVC-H0059 by HiSilicon
- Simulation results
 - No BD-rate loss for both IHE and ILC

2. Algorithm



Algorithm

- Remove intra prediction mode #34
- No modification for 4x4 blocks



3. Experiments



Experimental results

- No BD-rate loss for both IHE and ILC

	All Intra HE			All Intra LC		
	Y	U	V	Y	U	V
Class A (8bit)	0.1%	0.1%	0.1%	0.1%	0.1%	0.1%
Class B	0.0%	0.0%	0.0%	0.0%	0.0%	0.0%
Class C	0.0%	0.0%	0.0%	0.0%	0.0%	0.0%
Class D	0.0%	0.1%	0.0%	0.0%	0.0%	0.0%
Class E	0.0%	0.0%	0.0%	0.0%	0.0%	0.0%
Overall	0.0%	0.0%	0.0%	0.0%	0.0%	0.0%
	0.0%	0.0%	0.0%	0.0%	0.0%	0.0%
Class F	0.0%	0.1%	0.0%	0.0%	0.0%	0.0%
Enc Time[%]	101%			101%		
Dec Time[%]	100%			101%		

4. Conclusion

Conclusion

- Advantage
 - Remove escape code for remaining intra prediction mode coding
 - Minimum WD/HM change
 - No BD-rate loss
- Recommendation
 - Remove escape code for remaining intra prediction mode
 - Discuss with other relevant contributions

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