

Potential enhancement of signaling of I_PCM blocks (JCTVC-G118/ M21668)

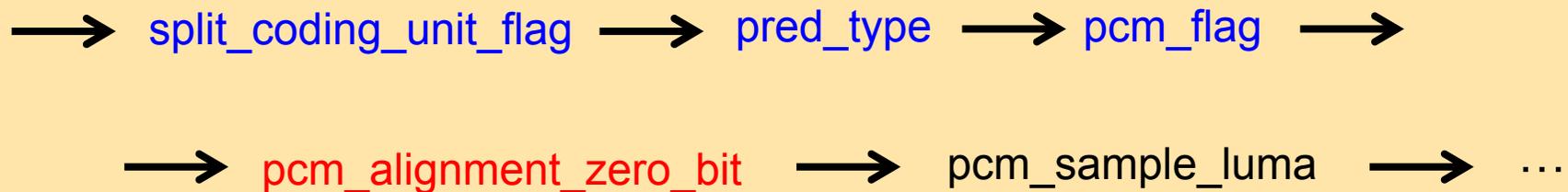
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Summary

- Review of I_PCM mode
- Signaling issue of successive I_PCM blocks
- Burst transmission of successive I_PCM blocks
- Recommend the study and test of proposal, preferably in context of CE

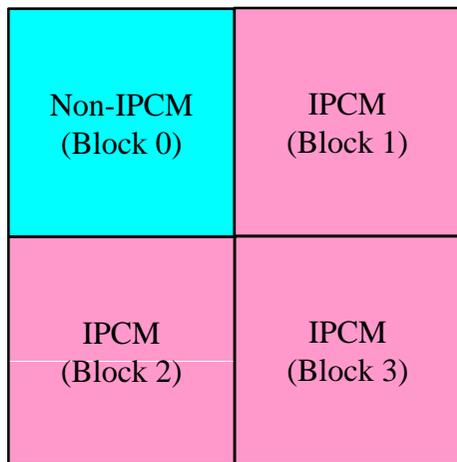
Signaling of I_PCM Block

- Presence of `pcm_flag=1` indicates that CU is I_PCM block.
- `pcm_flag` syntax presents after CU/PU header parameters such as `split_coding_unit_flag`, `pred_type`, etc.
- `pcm_alignment_zero_bit` and PCM sample data follow it.

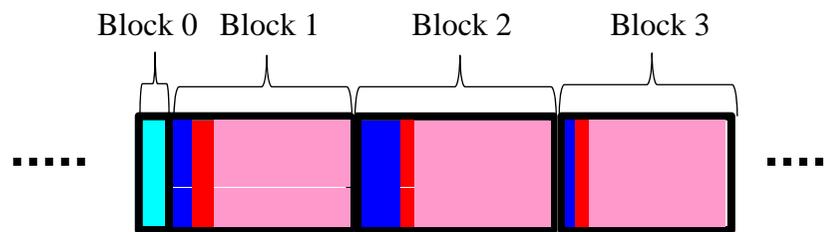


Signaling Issue of Successive I_PCM Blocks

- Transmission of PCM sample data of each of successive I_PCM blocks is terminated.
 - Sub-optimal throughput of transmitting PCM sample data
 - Increase bits of side-information



(a)



■ **pred_type, pcm_flag, etc.**

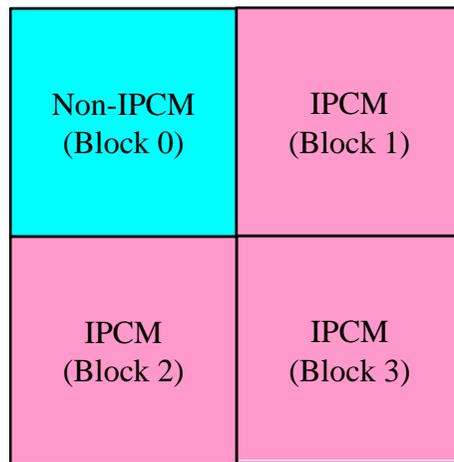
■ **pcm_alignment_zero_bit**

■ **PCM sample data**

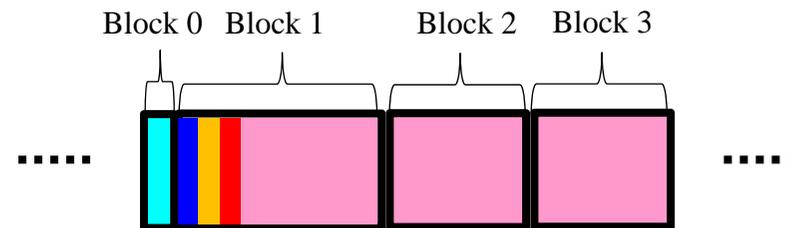
(b) HM bitstream of (a)

Burst Transmission of Successive I_PCM Blocks

- Signal the number of subsequently coded I-PCM blocks at the PU header of the first I_PCM block
- PCM sample data of successive I_PCM blocks follow the pcm_alignment_zero_bit of the first I_PCM block.
 - Optimal throughput of transmitting PCM sample data
 - Decrease bits of side-information



(a)



- `pred_type, pcm_flag, etc.`
- `subsequent_pcm_num`
- `pcm_alignment_zero_bit`
- PCM sample data

(b) Proposal's bitstream of (a)

Conclusions

- Review of I_PCM mode
- Signaling issue of successive I_PCM blocks
- Burst transmission of successive I_PCM for the optimal throughput of transmitting PCM sample data
- Recommend the study and test of proposal, preferably in context of CE

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