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| *Title:* | **CE10: Crosscheck of Samsung/FastVDO’s contribution on core transform - low QP range (JCTVC-F363)** | | |
| *Status:* | Input Document to JCT-VC | | |
| *Purpose:* | Information | | |
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# Abstract

The contribution presents an independent cross-check of JCTVC-F363 for low QP range per CE10 plans. The simulations results match those reported by the proponents.

# Introduction

JCTVC-F363 [1] presents Samsung and FastVDO’s proposal for a core transform design that can support three forms of implementation: matrix multiplication, partial butterfly, and full-factorization.

# Simulation Results

We ran codec simulations using the Samsung/FastVDO’s software on the low QP settings agreed to in CE10. Due to limited time and computational resources, we did not run the anchor and relied on the anchor results provided by Samsung/FastVDO. Therefore we do not report run times. We verified that the results match those provided by Samsung/FastVDO.

# References

1. W. Dai, et. al., “CE10: FastVDO-Samsung Core Transform Proposal,” JCT-VC Document, JCTVC-F251, Torino, July 2011.