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| *Title:* | **CE2: Crosscheck of Qualcomm’s proposal of JCTVC-D368 by MediaTek** | | |
| *Status:* | Input Document to JCT-VC | | |
| *Purpose:* | Report | | |
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| *Source:* | MediaTek Inc. | | |

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# Abstract

The purpose of this document is to crosscheck the Qualcomm’s proposal of D368 in CE2[1]. The verification task has been done successfully and the BD-rates match those provided by Qualcomm exactly.

# Introduction

Qualcomm provided the TMuC0.9-based source code of their proposed algorithm. Two configurations (random access and Low delay) were tested for the provided code.

# Test Results

The BD-rates from our experimental results match those provided by Qualcomm exactly. The detailed results are listed in JCTVC-D427.xls.

# Conclusion

The results of Qualcomm in D368 are confirmed.

# References

1. E. Francois, X. Zheng, P. Chen, “Description of Core Experiment 2: Flexible Motion Partitioning,” JCTVC-C502, Guangzhou, Oct. 2010.