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| *Title:* | **Some errata items for HEVC** | | |
| *Status:* | Input Document to JCT-VC | | |
| *Purpose:* | Proposal | | |
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# Abstract

This contribution proposes some errata changes to the HEVC specification.

# Proposal

## Definitions of tile, tile column, tile row, and tile scan

As can be seen from clause 6.3.1, a tile consists of CTUs. To be consistent, the definitions of tile, tile column, tile row, and tile scan are proposed to changed as follows.

*In 3, replace the definitions of tile, tile column, tile row, and tile scan with the following:*

**tile**: A rectangular region of *~~CTBs~~ CTUs* within a particular *tile column* and a particular *tile row* in a *picture*.

**tile column**: A rectangular region of *~~CTBs~~ CTUs* having a height equal to the height of the *picture* and a width specified by *syntax elements* in the *picture parameter set*.

**tile row**: A rectangular region of *~~CTBs~~ CTUs* having a height specified by *syntax elements* in the *picture parameter set* and a width equal to the width of the *picture*.

**tile scan**: A specific sequential ordering of *~~CTBs~~ CTUs* *partitioning* a *picture* in which the *~~CTBs~~ CTUs* are ordered consecutively in *~~CTBs~~ CTUs* *raster scan* in a *tile* whereas *tiles* in a *picture* are ordered consecutively in a *raster scan* of the *tiles* of the *picture*.

## Missing value range for the ue(v)-coded delta\_poc\_msb\_cycle\_lt[ i ]

*In 7.3.6, replace the semantics of delta\_poc\_msb\_cycle\_lt[ i ] with the following:*

**delta\_poc\_msb\_cycle\_lt**[ i ] is used to determine the value of the most significant bits of the picture order count value of the i-th entry in the long-term RPS of the current picture. The value of delta\_poc\_msb\_cycle\_lt[ i ] shall be in the range of 0 to 2(32 − log2\_max\_pic\_order\_cnt\_lsb\_minus4 − 4 ), inclusive. When delta\_poc\_msb\_cycle\_lt[ i ] is not present, it is inferred to be equal to 0.

The variable DeltaPocMsbCycleLt[ i ] is derived as follows:

if( i = = 0 | | i = = num\_long\_term\_sps )  
 DeltaPocMsbCycleLt[ i ] = delta\_poc\_msb\_cycle\_lt[ i ]  
else (7-52)  
 DeltaPocMsbCycleLt[ i ] = delta\_poc\_msb\_cycle\_lt[ i ] + DeltaPocMsbCycleLt[ i − 1 ]

## Semantics of num\_ref\_idx\_l1\_default\_active\_minus1

*In 7.4.3.3.1, replace the semantics of num\_ref\_idx\_l0\_default\_active\_minus1 and num\_ref\_idx\_l1\_default\_active\_minus1 with the following:*

**num\_ref\_idx\_l0\_default\_active\_minus1** specifies the inferred value of num\_ref\_idx\_l0\_active\_minus1 for P and B slices with num\_ref\_idx\_active\_override\_flag equal to 0. The value of num\_ref\_idx\_l0\_default\_active\_minus1 shall be in the range of 0 to 14, inclusive.

**num\_ref\_idx\_l1\_default\_active\_minus1** specifies the inferred value of num\_ref\_idx\_l1\_active\_minus1 for B slices with num\_ref\_idx\_active\_override\_flag equal to 0. The value of num\_ref\_idx\_l1\_default\_active\_minus1 shall be in the range of 0 to 14, inclusive.

## Semantics of delta\_poc\_s0\_minus1[ i ]

*In 7.4.8, replace the semantics of delta\_poc\_s0\_minus1[ i ] with the following:*

**delta\_poc\_s0\_minus1**[ i ] plus 1, when i is equal to 0, specifies the difference between the picture order count values of the current picture and the i-th entry in the stRpsIdx-th candidate short-term RPS that has picture order count value less than that of the current picture, or, when i is greater than 0, specifies the difference between the picture order count values of the ( i − 1 )-th entry and the i-th entry in the stRpsIdx-th candidate short-term RPS that have picture order count values less than the picture order count value of the current picture. The value of delta\_poc\_s0\_minus1[ i ] shall be in the range of 0 to 215 − 1, inclusive.

# Patent rights declaration(s)

**Huawei Technologies Co., Ltd., does not have any current or pending patent rights relating to the technology described in this contribution (to the extent of the personal awareness of the authors).**